

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: James D. Ballew, et al.  
Serial No.: 10/826,959  
Filing Date: April 15, 2004  
Art Unit: 2113  
Confirmation No.: 1484  
Examiner: Elmira Mehrmanesh  
Title: *SYSTEM AND METHOD FOR DETECTING  
AND MANAGING HPC NODE FAILURE*

**MAIL STOP RCE**  
Commissioner of Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INFORMATION DISCLOSURE STATEMENT**

Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application.

The present application was filed after June 30, 2003. Therefore, under the July 11, 2003 waiver of 37 C.F.R. § 1.98(a)(2)(i) by the U.S. Patent and Trademark Office, copies of the U.S. Patents and U.S. Patent Application Publications listed in the attached PTO-1449 form are not enclosed. Copies of all other references are enclosed for the convenience of the Examiner.

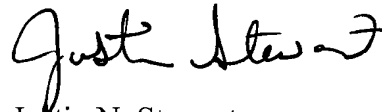
Pursuant to 37 C.F.R. §§1.97(g) and (h), Applicants make no representation that these documents qualify as prior art or that these documents are material to patentability of the present application or that a search has been made.

**REMARKS**

This Information Disclosure Statement is being filed concurrently with a Request for Continued Examination and therefore Applicants believe no fee is due. However, if a fee is required, the Commissioner is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

Baker Botts L.L.P.  
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Date: May 4, 2009

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